



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER OF PATENTS AND TRADEMARKS  
Washington, D.C. 20231  
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/008,241	11/08/2001	Ralf Otremba	WMP-IFT618	6971

7590 07/18/2002

LERNER AND GREENBERG, P.A.  
Post Office Box 2480  
Hollywood, FL 33022-2480

EXAMINER

MCCAMEY, ANN M

ART UNIT

PAPER NUMBER

2833

DATE MAILED: 07/18/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)
	10/008,241	OTREMBA ET AL.
	Examiner Ann M McCamey	Art Unit 2833
<i>-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --</i>		
<b>Period for Reply</b>		
<b>A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.</b>		
<ul style="list-style-type: none"> <li>- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.</li> <li>- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.</li> <li>- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.</li> <li>- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).</li> <li>- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).</li> </ul>		
<b>Status</b>		
1) <input type="checkbox"/> Responsive to communication(s) filed on _____.		
2a) <input type="checkbox"/> This action is FINAL.                    2b) <input checked="" type="checkbox"/> This action is non-final.		
3) <input type="checkbox"/> Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.		
<b>Disposition of Claims</b>		
4) <input checked="" type="checkbox"/> Claim(s) <u>1-10</u> is/are pending in the application.		
4a) Of the above claim(s) _____ is/are withdrawn from consideration.		
5) <input type="checkbox"/> Claim(s) _____ is/are allowed.		
6) <input checked="" type="checkbox"/> Claim(s) <u>1-10</u> is/are rejected.		
7) <input type="checkbox"/> Claim(s) _____ is/are objected to.		
8) <input type="checkbox"/> Claim(s) _____ are subject to restriction and/or election requirement.		
<b>Application Papers</b>		
9) <input type="checkbox"/> The specification is objected to by the Examiner.		
10) <input checked="" type="checkbox"/> The drawing(s) filed on <u>08 November 2001</u> is/are: a) <input checked="" type="checkbox"/> accepted or b) <input type="checkbox"/> objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).		
11) <input type="checkbox"/> The proposed drawing correction filed on _____ is: a) <input type="checkbox"/> approved b) <input type="checkbox"/> disapproved by the Examiner. If approved, corrected drawings are required in reply to this Office action.		
12) <input type="checkbox"/> The oath or declaration is objected to by the Examiner.		
<b>Priority under 35 U.S.C. §§ 119 and 120</b>		
13) <input checked="" type="checkbox"/> Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) <input checked="" type="checkbox"/> All b) <input type="checkbox"/> Some * c) <input type="checkbox"/> None of: 1. <input checked="" type="checkbox"/> Certified copies of the priority documents have been received. 2. <input type="checkbox"/> Certified copies of the priority documents have been received in Application No. _____. 3. <input type="checkbox"/> Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.		
14) <input type="checkbox"/> Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application). a) <input type="checkbox"/> The translation of the foreign language provisional application has been received.		
15) <input type="checkbox"/> Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.		
<b>Attachment(s)</b>		
1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)		
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)		
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) <u>4</u> .		
4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____.		
5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)		
6) <input type="checkbox"/> Other: _____.		

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-8 and 10 are rejected under 35 U.S.C. 102(b) as being anticipated by Gazda et al. (US 5,249,978).

Regarding claims 1 and 10, Gazda et al. disclose an electronic component/transistor comprising:

a housing 5, 9 having a first side face; and  
at least two terminal pins 21 protruding out of said first side face of said housing;  
said housing having an isolation barrier 43 formed between said two terminal pins.

Regarding claim 2, Gazda et al. disclose said isolation barrier being integrally formed onto said housing.

Regarding claims 3 and 4, Gazda et al. disclose said first side face having a first edge and a second edge opposite said first edge; and said isolation barrier being a plate extending on said first side face from said first edge to said second edge.

Regarding claim 5, Gazda et al. disclose said isolation barrier having a side remote from said first side face of said housing; and said two terminal pins protruding beyond said side of said isolation barrier.

Regarding claim 6, Gazda et al. disclose the housing having at least one side wall that together with said isolation barrier encloses said two terminals from at least three sides.

Regarding claim 7, Gazda et al. disclose at least two terminal pins protruding out of said housing next to each other in a row.

Regarding claim 8, Gazda et al. disclose said first side face having a first edge and a second edge opposite said first edge; a first one of said terminal pins being located closer to said first edge than a second one of said two terminal pins; and said second one of said two terminal pins being located closer to said second edge than said first one of said two terminal pins.

Claims 1-4, 6 and 8-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Peterson (US 5,342,221).

Regarding claims 1 and 10, Peterson discloses (Fig. 2) an electronic component/transistor comprising:

a housing 16 having a first side face; and  
at least two terminal pins 126 protruding out of said first side face of said housing;

said housing having an isolation barrier 66 formed between said two terminal pins.

Regarding claim 2, Peterson discloses said isolation barrier being integrally formed onto said housing.

Regarding claims 3 and 4, Peterson discloses said first side face having a first edge and a second edge opposite said first edge; and said isolation barrier being a plate extending on said first side face from said first edge to said second edge.

Regarding claim 6, Peterson discloses the housing having at least one side wall that together with said isolation barrier encloses said two terminals from at least three sides.

Regarding claim 8, Peterson discloses said first side face having a first edge and a second edge opposite said first edge; a first one of said terminal pins being located closer to said first edge than a second one of said two terminal pins; and said second one of said two terminal pins being located closer to said second edge than said first one of said two terminal pins.

Regarding claim 9, Peterson discloses a plurality of isolation barriers that includes said isolation barrier; said housing having side walls that together with said plurality of said isolation barriers surrounds at least one of said two terminal pins from four sides.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ann M McCamey whose telephone number is (703) 305-3422. The examiner can normally be reached on M-F 9-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Paula A. Bradley can be reached on (703) 308-2319. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

AMM  
July 9, 2002

  
Ann M. McCamey  
Patent Office